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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE


OCT 10 2005

Application Serial No. 10/626,762
Filing Date July 24, 2003
Inventor Walker, et al.
Assignee IBM Corporation
Group Art Unit 2825
Examiner Leigh M. Garbowski
Attorneys Docket No. YOR920030303US1
Title: SYSTEM AND METHOD FOR DERIVATIVE-FREE OPTIMIZATION OF ELECTRICAL
CIRCUITS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this Response to Restriction Requirement (1 page) is being
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914-945-2488

10/10/05

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RESPONSE TO RESTRICTION REQUIREMENT

To: Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
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From: Satheesh K. Karra
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REMARKS

In response to the October 3, 2005 Restriction Requirement, Applicants elect Group I, Claims 1-13, and 32 - 38 for prosecution on the merits. Applicants reserve the right to file one or more divisional applications directed to the subject matter of the non-elected claims.

The undersigned is available for telephone consultation at any time.

Respectfully submitted,

Dated: October 10, 2005

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